

Amendments to the Claims:

The following listing of claims will replace all prior versions, and listings, of claims in the application:

- 1-20. (Canceled)
21. (New) An electronic device comprising:
 - a plurality of first signal lines;
 - a plurality of second signal lines;
 - a plurality of power source lines, the plurality of power source lines extending along a direction in which the plurality of first signal lines extend; and
 - a plurality of unit circuits,
 - each of the plurality of unit circuits including a first transistor,
 - a conduction state of the first transistor being set by a data current that flows between one power source line of the plurality of power source lines and one second signal line of the plurality second signal lines, and
 - the data current flowing between the one power source line and the one second signal line during a first period.
22. (New) The electronic device according to claim 21,
 - the data current flowing through the first transistor during the first period.
23. (New) The electronic device according to claim 21,
 - each of the plurality of unit circuits further including a second transistor,
 - the first transistor including a first terminal, a second terminal, and a first control terminal,
 - the second transistor including a third terminal, a fourth terminal, and a second control terminal, and
 - the data current flowing through the first transistor and the second transistor.

24. (New) The electronic device according to claim 21,
each of the plurality of unit circuits further including a second transistor, a
third transistor, and an electronic element,
the first transistor including a first terminal, a second terminal, and a first
control terminal,
the second transistor including a third terminal, a fourth terminal, and a second
control terminal,
the electronic element including a fifth terminal and a sixth terminal, the fifth
terminal being coupled to the first terminal,
the sixth terminal being set to a plurality of electronic potentials or to be
electrically connected to a predetermined electric potential and electrically disconnected from
the predetermined electric potential.

25. (New) The electronic device according to claim 21,
the data current not flowing through the electronic element during the first
period.

26. (New) The electronic device according to claim 21,
further comprising a control circuit,
each of the plurality of unit circuits further including a second transistor, a
third transistor, and an electronic element,
the first transistor including a first terminal, a second terminal, and a first
control terminal,
the second transistor including a third terminal, a fourth terminal, and a second
control terminal,

the electronic element including a fifth terminal and a sixth terminal, the fifth terminal being coupled to the first terminal, the sixth terminal being coupled to an electric potential control line,

the third transistor to control electrical connection between the first terminal and the first control terminal, and

the control circuit setting the electrical potential line to a plurality of electric potentials, or the control circuit controlling electrical connection and electrical disconnection between the electric potential control line and a predetermined electric potential.

27. (New) The electronic device according to claim 23,

each of the plurality of unit circuits further including a third transistor that controls electrical connection between the first terminal and the first control terminal,

transistors included in each of the plurality of unit circuits being only the first transistor, the second transistor and the third transistor.

28. (New) The electronic device according to claim 21,

each of the plurality of unit circuits further including an electronic element that is coupled to the first transistor, and

the electronic element being a current driven element.

29. (New) The electronic device according to claim 23,

each of the plurality of unit circuits further including a capacitive element that is coupled to the first control terminal.

30. (New) The electronic device according to claim 26,

the control circuit being a fourth transistor including a ninth terminal and a tenth terminal,

the ninth terminal being coupled to the sixth terminal through the electric potential control line, and

the tenth terminal being coupled to a supply line to supply the plurality of electric potentials or the predetermined potential.

31. (New) The electronic device according to claim 21,
further comprising a plurality of electro-optical elements,
the plurality of first signal lines being a plurality of scanning lines,
the plurality of second signal lines being a plurality of data lines, and
a gray scale level of each of the plurality of electro-optical elements being
determined according to the conduction state of the first transistor.
32. (New) The electronic device according to claim 31,
the electro-optical element being an EL element.
33. (New) The electronic device according to claim 31,
the plurality of electro-optical elements including a plurality of groups each of
which includes electro-optical elements whose color is same and that are disposed along one
scanning line of the plurality of scanning lines.
34. (New) An electronic apparatus comprising the electronic device according to
claim 21.
35. (New) A method of driving an electronic device including a plurality of first
signal lines, and a plurality of power source lines, a group of unit circuits that are disposed
along one first signal line of the plurality first signal lines and one power source line of the
plurality of power source lines, the group of unit circuits including first transistors and second
transistors, the method comprising:

supplying a first signal that puts the second transistors into on-states during at
least a part of a first period; and

supplying data currents that flow through the second transistors and the first transistors between the one power source line and a plurality of second signal lines during at least a part of the first period,

a conduction state of each of the first transistors being set by a data current of the data currents.

36. (New) The method according to claim 35,
the plurality of second signal lines intersect the one first signal line and the one power source lines.

37. (New) The method according to claim 35,
further comprising supplying at least one of a driving current and a driving voltage to an electronic element that is formed for each of the first transistors,
at least one of a current level of the driving current and a voltage level of the driving voltage being determined by the conduction state of the first transistor.

38. (New) The method according to claim 37,
the electronic element being an electro-optical element, and
a gray scale level of the electro-optical element being determined by the conduction state of the first transistor.

39. (New) The method according to claim 35,
each of the plurality of unit circuits including a third transistor that controls an electrical connection and an electrical disconnection between a first terminal and a first control terminal, and
the first terminal and the first control terminal being included in a first transistor of the first transistors.

40. (New) The method according to claim 39,

further comprising putting the third transistor an on-state so as to electrically connect the first terminal to the first control terminal during at least a part of the first period.

41. (New) The method according to claim 37,
no current being supplied to the electronic element during at least a part of the first period.